

ABSTRACT OF THE DISCLOSURE

The power consumption of an image pickup apparatus using a solid-state image pickup device is reduced. At the termination of a readout period of an information charge from a CCD image sensor (2), a timing generator (8) stops voltage booster pulses for boosting the output voltage of a power supply (6). As a result, a driver (4) stops operation and the power consumption required to drive the CCD image sensor (2) basically stops. Thereafter, as the electronic shutter operation approaches, the timing generator (8) resumes the generation of the voltage booster pulses and the voltage of the power supply (6) is boosted. On the basis of the exposure condition of the previous field, a DSP (16) obtains the timing for the electronic shutter for the next field, and further the timing that has been advanced only for the period required for voltage boosting set in a register, and starts the voltage boosting operation from this advanced timing. The voltage booster pulses are consecutively generated in the voltage booster period to rapidly raise the voltage of the power supply (6).